

FIG. 1

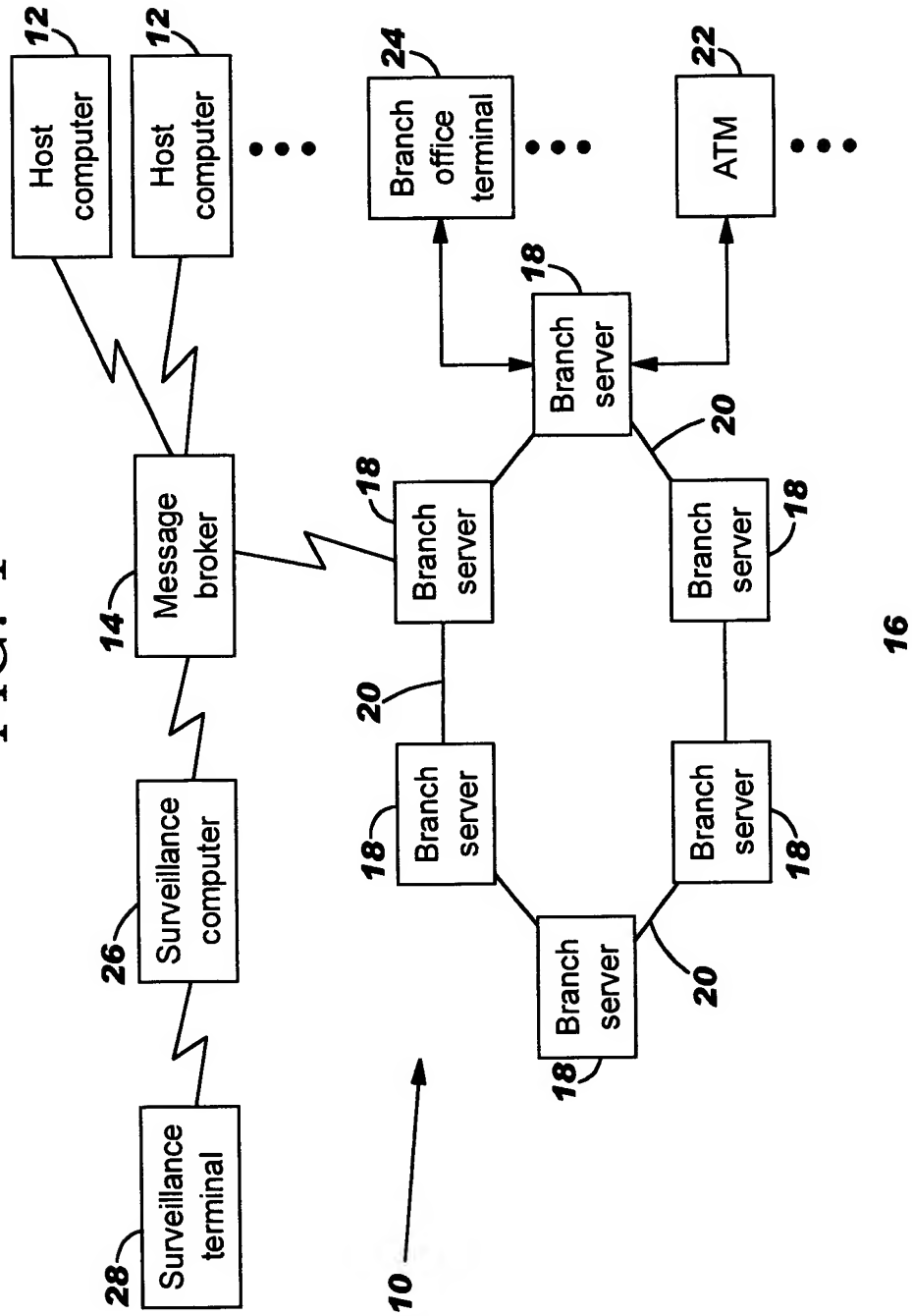


FIG. 2A

MQMD	MB header	Input APPL header 1	Input APPL header 2	Text
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FIG. 2B

SAIL header	Input APPL header 1	Input APPL header 2	Text
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FIG. 2C

SAIL header	Output APPL header 1/2	Text
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FIG. 2D

MQMD	MB header	Output APPL header 1/2	Text
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FIG. 3

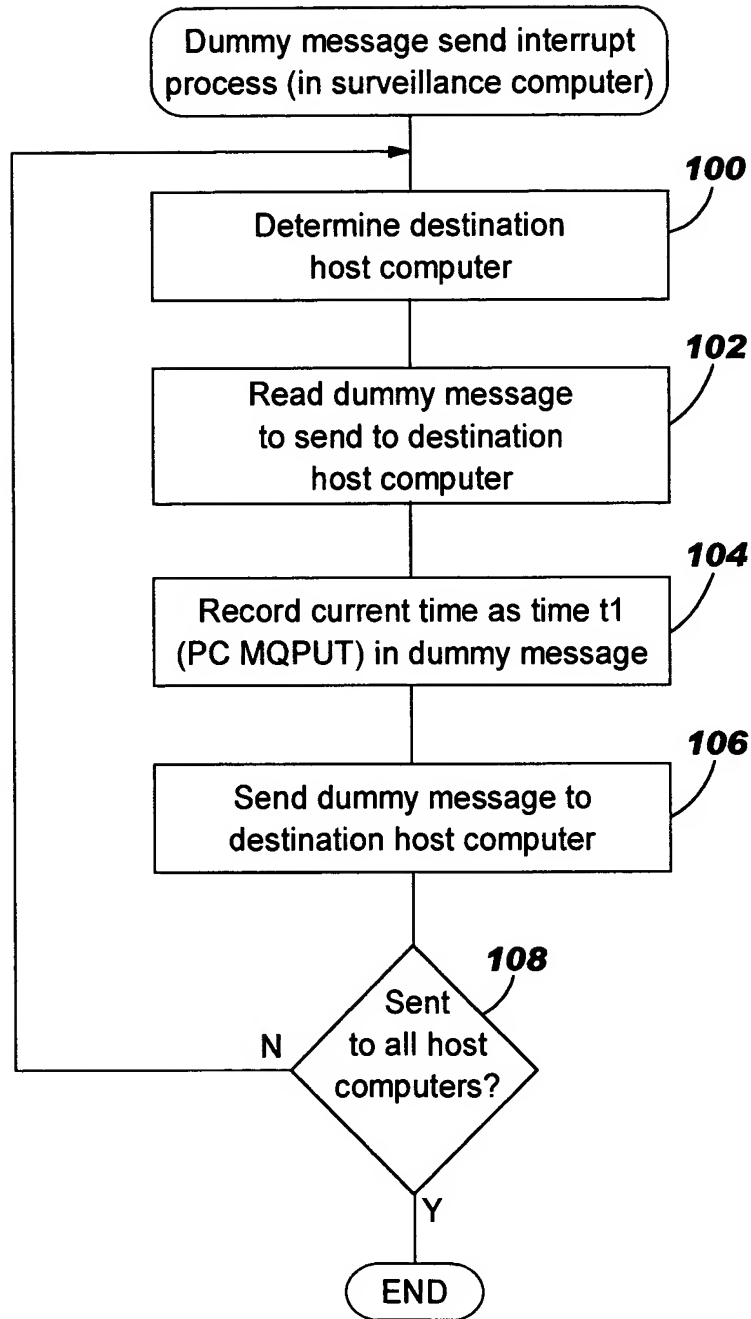


FIG. 4

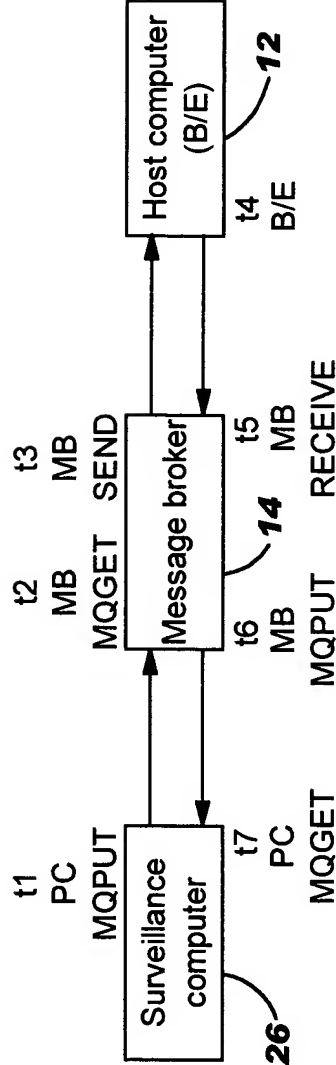


FIG. 5

